

**Notice of References Cited**

Application/Control No.

10/602,436

Applicant(s)/Patent Under  
Reexamination  
LEE ET AL.

Examiner

Thomas J. Magee

Art Unit

2811

Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,441,901	08-1995	Candelaria, Jon J.	438/317
	B	US-5,241,197	08-1993	Murakami et al.	257/192
	C	US-6,838,322 B2	01-2005	Pham et al.	438/151
	D	US-6,803,631 B2	10-2004	Dakshina-Murthy et al.	257/349
	E	US-6,800,910 B2	10-2004	Lin et al.	257/410
	F	US-6,350,993 B1	02-2002	Chu et al.	257/19
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	S. Wolf and R.N. Tauber, "Silicon Processing for the VLSI Era, Volume 1 - Process Technology," Lattice Press, Sunset Beach (1986) p. 325			
	V				
	W				
	X				

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.